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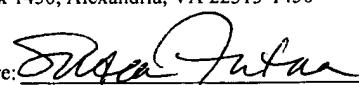
Attorney Docket No. YOR920030410US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s) Daniel C. Worledge  
Docket No.: YOR920030410US1  
Serial No.: 10/699,284  
Filing Date: October 31, 2003  
Group: 2818  
Examiner: Thinh T. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:  Date: October 12, 2005

Title: Techniques for Coupling in Semiconductor Devices

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TRANSMITTAL LETTER

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application: Submitted herewith is a Response to an Office Action relating to the above-identified patent application. There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,



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Dated: October 12, 2005



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RESPONSE TO OFFICE ACTION

20     Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

25     Sir:

In response to the outstanding Office Action, dated July 12, 2005, please amend the above-identified patent application as follows.